PRODUCTION OF SINGLE CRYSTAL SILICON INGOT AND WAFER BY ADJUSTING PULLING UP SPEED PROFILE IN HOT ZONE AND INGOT AND WAFER PRODUCED BY THE SAME

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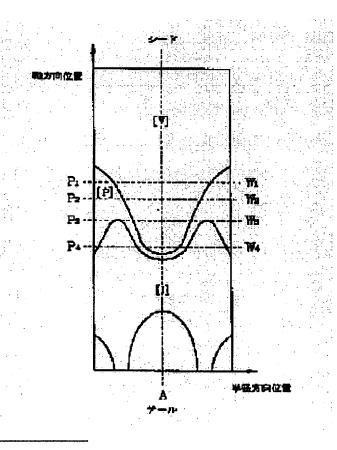
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Abstract of **JP11001393**

PROBLEM TO BE SOLVED: To provide a method for producing a microelectronic element and an apparatus therefor, in detail, a method for producing a silicon ingot and to obtain a silicon ingot and a wafer produced by the method. SOLUTION: A silicon ingot is produced by pulling up an ingot in an axial direction from a melt in a hot zone furnace by an ingot pulling speed profile which is sufficiently high to prevent an interstitial mass and is sufficiently low to restrict a vacancy mass in a zone rich in vacancy. The ingot thus pulled up is sliced into zones rich in vacancy containing each vacancy mass at the center and plural semi-zero defect wafers having a zero defect zone free from a vacancy mass and an interstitial mass though being positioned between the zone rich in vacancy and the edge part of the wafers.



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